



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of:

T. NAKAMURA

Application No.: 09/922,742

Filed: August 7, 2001

Group Art Unit: 2188

Examiner: P. Vital

Docket No.: 108066-00038

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SEP 08 2003

Technology Center 2100

For: CLOCK SYNCHRONIZED DYNAMIC MEMORY AND CLOCK SYNCHRONIZED  
INTEGRATED CIRCUIT

**RESPONSE UNDER 37 C.F.R. § 1.121**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 4, 2003

Sir:

**INTRODUCTORY COMMENTS**

In reply to the Office Action mailed June 4, 2003, please amend the above-identified application as follows.